

AUTOMATIC REFERENCE VOLTAGE REGULATION IN A MEMORY DEVICE

RELATED APPLICATION

[0001] This Application is a Continuation of U.S. Application Serial No. 10/298,830 filed November 18, 2002, ^{now Patent No. 6,738,298} which is incorporated herein by reference.

BACKGROUND OF THE INVENTION

I. FIELD OF THE INVENTION

[0002] The present invention relates generally to reference voltage adjustments and particularly to adjustment of a reference voltage in a memory device.

II. DESCRIPTION OF THE RELATED ART

[0003] Memory devices are typically provided as internal storage areas in computers. The term memory identifies data storage that comes in the form of integrated circuit chips. There are currently many different types of memory.

[0004] One type is random access memory (RAM). This is typically used as the main memory in a computer system. RAM refers to memory that can be both written to and read from. This is in contrast to read only memory (ROM) that permits data to only be read. Most RAM is volatile meaning that it requires a steady flow of power to maintain its contents. When power is turned removed, the data in RAM is lost.

[0005] An electrically erasable programmable read-only memory (EEPROM) is a special type of non-volatile ROM that can be erased a byte at a time by exposing it to an